

**Application Data Sheet**

**Application Information**

Application Type::	Regular
Subject Matter::	Nationalization
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	INTEGRATED CIRCUIT DESIGN METHOD, DESIGN ASSISTANCE PROGRAM AND INTEGRATED CIRCUIT DESIGN SYSTEM USING SUCH INTEGRATED CIRCUIT DESIGN METHOD
Attorney Docket Number::	HIR-0040
Request for Early Publication?::	No
Request for Non-Publication?::	No
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

**Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
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### **Correspondence Information**

Correspondence Customer Number:: 23353

### **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/JP2005/002047	2/10/2005

### **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2004-033943	2/10/2004	Yes

### **Assignee Information**

Assignee name:: Japanese Science and Technology Agency  
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